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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:	Lin, *et al.*	Attorney Docket:	TSM03-0499
Filed:	October 3, 2003	Examiner:	TBD
Serial No.:	10/678,710	Art Unit:	TBD
For:	Dual Fully-Silicided Gate MOSFETs		

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

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Kristin Hayes — 10/27/03
Date

INFORMATION DISCLOSURE STATEMENT

The Applicant wishes to bring to the attention of the Patent and Trademark Office the information noted on the enclosed form SB-08A that may be considered material to the examination of the above-identified application.

No fee is due at this time, as this Information Disclosure Statement is being filed pursuant to 37 C.F.R. § 1.97(b)(1), within three months of the filing date of a national application other than a continued prosecution application under § 1.53(d).

Cite numbers 1-3 are newly cited prior art being filed under the waived requirement of 37 CFR 1.98 (a)(2)(i) dated July 11, 2003, therefore, copies of these U.S. patents are not included.

Respectfully submitted,

10-27-03
Date

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet 1 of 1

Complete if Known	
Application Number	10/678,710
Filing Date	October 3, 2003
First Named Inventor	Lin, *et al.*
Art Unit	TBD
Examiner Name	TBD
Attorney Docket Number	TSM03-0499

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No.[1]	Document Number: Number - Kind Code[2] (*if known*)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	1	US-6,465,309 B1	10-15-2002	Xiang, *et al.*	
	2	US-6,475,874 B2	11-05-2002	Xiang, *et al.*	
	3	US-6,518,154 B1	02-11-2003	Buynoski, *et al.*	
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FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No.[1]	Foreign Patent Document: Country Code[3] - Number[4] - Kind Code[5] (*if known*)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T[6]

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.[1]	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T[2]
	4	TAVEL, B., *et al.*, "Totally Silicided ($CoSi_2$) Polysilicon: A Novel Approach To Very Low-Resistive Gate (~2Ω/□) Without Metal CMP Nor Etching" IEDM (March 2001) pp. 825-828.	
	5	MASZARA, W.P., *et al.*, "Transistors With Dual Work Function Metal Gates By Single Full Silicidation (FUSI) Of Polysilicon," IEDM (February 2002) pp. 367-370.	

Examiner Signature		Date Considered	

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. [1]Applicant's unique citation designation number (optional). [2]See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. [3]Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). [4]For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. [5]Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. [6]Applicant is to place a check mark here if English language Translation is attached.

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